

About the Institution

The college was established in 1995 and is situated 10 km away from Nandyal on NH-40 towards Kurnool. It has been setup with the aim to achieve technology excellence through innovation and with a motto of "EDUCATION for PEACE". It is approved by AICTE and accredited by NBA (Tier I – all eligible UG programs), New Delhi, Affiliated to JNTUA, Ananthapuramu, Accredited by NAAC with A+ Grade, New Delhi UGC Conferred CPE Status, APSSDC SIEMENS-TSDI, Recognized UGC DDU Kaushal Kendra. The college offers 9 U.G. programs, 7 P.G. programs and 7 research programs. The Institution possesses very good infrastructure and faculty.

About the Department

The Department of Electrical & Electronics Engineering (EEE) has been started in 1995, with an intake of 60 students and currently intake has increased to 120. The Department is offering M.Tech in Electrical Drives and Control with a sanctioned intake of 18. Department also offers fulltime Ph.D from the year 2014 and recognized as research center by JNTUA, Anantapuramu. The major strength of the Department is that both the students and faculty are involving continuously in research activities, paper presentations at various International Conferences and Journals. To the credit of the Department has a credit of sanctioned DST-FIST (60.0 Lakhs), SERB (41.0 Lakhs), AICTE-RPS (29.0 Lakhs), AICTE-MODROBS (20.0 Lakhs), AICTE-SG/FDP (8.0 Lakhs). The department is equipped with state of-the-art laboratory facilities, including advanced testing equipment, simulation tools, and research infrastructure.

AICTE Training & Learning (ATAL) Academy

The AICTE Training and Learning (ATAL) Academy, under the Ministry of Education, Government of India, aims to empower faculty by promoting access, equity, and quality in higher education. It focuses on equipping faculty and technicians with the latest tools and technologies to better train the next generation. The key objective is to improve technical education and foster research, innovation, and entrepreneurship through training in emerging fields. ATAL Academy offers various development opportunities, such as workshops, orientations, learning communities, peer mentoring, and other faculty development programs (FDPs).

Objective of the Program

The objective of this Faculty Development Program (FDP) is to provide in-depth knowledge on the design and practical implementation of digital controller-based islanding detection methods for photovoltaic (PV) grid-connected systems. The program aims to equip participants with advanced techniques to enhance grid reliability by accurately detecting islanding conditions with minimal false tripping. It will cover key aspects such as detection algorithms, compliance with grid standards, real-time performance, and hardware considerations. Through interactive sessions and hands-on demonstrations, the FDP will enable faculty members to integrate cutting-edge islanding detection techniques into their research and academic curriculum effectively.



AICTE Training and Learning Academy (ATAL)

AICTE, New Delhi

Sponsored

Six days Offline Faculty Development Programme

on

Design and Practical Insights into Digital Controller-Based Islanding Detection for PV Grid Systems

Date: 19th - 24th January, 2026

Timing: 09.30 AM to 05.00 PM



Organized by

**DEPARTMENT OF ELECTRICAL AND
ELECTRONICS ENGINEERING**

**Rajeev Gandhi Memorial College of
Engineering & Technology
(Autonomous)**

Nerawada X Roads, Nandyal-518501,
Andhra Pradesh, India

Outcomes of the Programme:

The Faculty Development Program (FDP) on "Design and Practical Insights into Digital Controller-Based Islanding Detection for PV Grid Systems" will enable participants to:

- **Gain In-Depth Knowledge** – Understand islanding detection techniques and digital controller-based methods.
- **Enhance Practical Skills** – Develop hands-on experience with real-time implementation and hardware integration.
- **Improve Research Capabilities** – Apply advanced detection methods in academic and industrial projects.
- **Ensure Grid Compliance** – Learn about regulatory standards and grid synchronization challenges.
- **Strengthen Teaching Methodologies** – Integrate cutting-edge concepts into curriculum and guide students effectively in renewable energy systems.

Organizing Committee

Chief Patrons



Dr. M. Santhiramudu
Chairman, RGM CET

Parton



Er. M. Sivaram
Managing Director, RGM CET

Convener



Dr. T. Jayachandra Prasad
Principal, RGM CET

Coordinator



Dr. J. Surya Kumari
Assoc. Professor, Dept. of EEE,
RGM CET

Co-Coordinator



Dr. D. Lenine
Professor, Dept. of EEE, RGM CET

About Registration

- No Charge for Registration, Course and Certification.
- Selection based on merit and first come, first serve basis.
- Only selected candidates will be informed by email/phone, therefore the candidates must provide a valid email ID and Contact number while doing registration.

Resource Persons

Resource Persons are experts from Industries, IIST, IIT, NIT, State, Central Universities.

Topics to be covered

- Islanding Detection Techniques: A Quick Recap
- Role of FPGAs Controllers in Islanding Detection
- Design Aspects of Controller-Based Islanding Detection
- Practical Implementation and Testing using Lab setup for validation
- Challenges in Real-World Deployment and Evaluation Metrics
- Advanced Insights/Modern Approaches
- Case Studies or Real-World Applications
- Key takeaways on designing reliable islanding detection systems

Important Dates

Last date for Registration : **14.01.2026**

Confirmation of Selection : **16.01.2026**

How to Apply?

The applicant should register at AICTE-ATAL web Portal at the earliest.

Website:

<https://atalacademy.aicteindia.org/login>

Target Audience

- Faculty Members of AICTE/UGC affiliated
- Institutes/Universities
- Research Scholars
- Persons working in R&D Organizations
- Industry Persons
- PG Students

Academic Experts



Dr. N. Venkata Ramana Naik

Assoc. Professor, Department of Electrical Engineering,
National Institute of Technology Rourkela, Odisha, India.
E-Mail: nenavathv@nitrkl.ac.in

Topic to be taught: Power Quality Improvement in Multifunctional Load-Grid Systems



Dr. Kaliamoorthy M

Professor / Department of EEE,
Dr.Mahalingam College of Engineering and Technology,
Pollachi, Coimbatore District, Tamil Nadu.
E mail: kaliasgoldmedal@gmail.com

Topic to be taught: Impact of Islanding on Grid Stability and Power Quality



Dr. Thulasingham Muthukumaran

Dy Manager (Electrical) NSIC LTD HYDERABAD
E-Mail: tmkumaran@nsic.co.in

Topic to be taught: Long-Term Performance Monitoring of Grid-Connected PV Plants



Dr. Srinivas Bhaskar Karanki

Assoc. Professor, School of Electrical and Computer Sciences
IIT Bhubaneswar
E-Mail: skaranki@iitbbs.ac.in

Topic to be taught: Control Strategy for Single-Phase Grid-Interfaced Modified Multilevel Inverter Topology for Distributed Power Generation

Industry Experts



Mr. Anvesh Reddy R

Manager,
765/400kV National Power Grid Kurnool,
E-Mail: anveshreddyrama@gmail.com

Topic to be taught: Standardized Testing Protocols for Grid-Connected Inverter Systems



Mr. Yuvaraj. S

Executive Trainee, JSK LAB Instruments, Chennai
E-Mail: vlsiraja@gmail.com

Topic to be taught: Practical Validation of Grid-Connected PV Systems Using Laboratory Test Bench



Dr. S. Senthil Kumar,

Professor / Department of EEE,
National Institute of Technology, Tiruchirappalli.
E mail: skumar@nitt.edu

Topic to be taught: Real-Time Simulation of Islanding Detection Methods in PV Systems



Mr. Hari Krishna K

Manager(Field),
Karnataka Solar Power Development Corporation Limited.
E-Mail: mfkspdcl@gmail.com

Topic to be taught: Battery-Integrated PV Systems: Deployment Issues and System Validation Metrics



Mr. Durga Vara Prasad

Assistant manager, Greenko Energy pvt ltd
E-Mail: dvaraprasad.d@greenkoassetmanagement.com
Topic to be taught: Hardware-Based Testing and Practical Evaluation of a Grid-Connected PV System